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## LAMPIRAN

### Lampiran 1.1 Hasil simulasi harmonic load flow

Project:	ETAP						Page:	14
Location:	19.0.1C						Date:	23-08-2023
Contract:							SN:	
Engineer:	Study Case: IIA						Revision:	Basis
Filename:	skripsi.moy						Config:	Nominal

  

System Harmonics Bus Information											
ID	Bus	kV	Voltage Distortion								
			Fund.	RMS	ASUM	THD	TIF	THIB	THBD	THDG	
Bus DIP		0.400	96.14	98.17	102.00	2.29	88.27	0.00	0.00	2.29	2.29
Bus MDF EMG		0.400	96.03	98.00	101.97	2.27	87.42	0.00	0.00	2.27	2.27
Bus PT Chiller		0.400	98.01	98.03	104.00	2.78	110.39	0.00	0.00	2.78	2.78
Bus PP CIXWPF		0.400	98.03	98.03	104.00	2.78	110.39	0.00	0.00	2.78	2.78
Bus PP-VAC		0.400	98.01	98.05	104.00	2.78	110.39	0.00	0.00	2.78	2.78
Bus PLTR 1		0.400	98.14	98.77	103.00	2.29	88.27	0.00	0.00	2.29	2.29
Bus PLTR 2		0.400	98.01	98.05	104.00	2.78	110.39	0.00	0.00	2.78	2.78
Bus SDP EMG PARKIR		0.400	97.99	98.01	101.44	2.29	88.27	0.00	0.00	2.29	2.29
Bus SDP PARKIR		0.400	98.14	98.17	103.00	2.29	89.27	0.00	0.00	2.29	2.29
Bus SDP Perpus PL		0.400	98.03	98.05	104.00	2.78	110.39	0.00	0.00	2.78	2.78
Bus17		20.000	100.00	100.01	102.40	1.18	28.93	0.00	0.00	1.18	1.18
Bus18		20.000	100.00	100.01	102.45	1.18	28.17	0.00	0.00	1.18	1.18
Bus94		20.000	99.99	100.00	102.40	1.17	28.99	0.00	0.00	1.17	1.17
Bus95		20.000	99.99	100.00	102.46	1.17	29.00	0.00	0.00	1.17	1.17
Bus102		20.000	99.99	100.00	102.44	1.18	28.21	0.00	0.00	1.18	1.18
Bus103		20.000	99.99	100.00	102.44	1.18	28.21	0.00	0.00	1.18	1.18

\* Indicates THD (Total Harmonic Distortion) Exceeds the Limit.  
# Indicates THD (Individual Harmonic Distortion) Exceeds the Limit.



**Lampiran 1.2 Hasil simulasi load flow analysis**

Project:	ETAP				Page:	10							
Location:	19.81C				Date:	23-08-2023							
Contract:					SN:								
Engineer:	Study Case: LF				Revision:	base							
Filename:	skripsiasey				Config.:	Normal							
<b>LOAD FLOW REPORT</b>													
Bus	Voltage			Generation	Load	Load Flow	XFMR						
ID	kV	% Mag.	Angl.	MW	Mvar	MW	Mvar	ID	MW	Mvar	Amp	%PF	%Tp
Bus DP	0.400	98.804	-2.8	0.000	0.000	1.303	0.006	Bus PUTR 1	-1.363	-0.486	2222.8	89.1	
								VTD61	0.007				
								VTD62	0.007	0.002	10.7	95.0	
								VTD63	0.015	0.003	23.7	95.0	
								VTD64	0.015	0.003	23.7	95.0	
								VTD65	0.015	0.003	23.7	95.0	
								VTD66	0.150	0.049	231.1	95.0	
								VTD67	0.070	0.025	317.1	95.0	
								VTD68	0.058	0.019	108.4	95.0	
								VTD69	0.068	0.022	193.0	95.0	
								VTD70	0.028	0.019	48.4	95.0	
								VTD71	0.058	0.019	48.4	95.0	
Bus MDP EMG	0.400	97.400	-3.0	0.000	0.000	0.431	0.247	Bus PUTR 1	-0.411	-0.247	736.7	86.8	
								VTD58	0.010	0.003	16.3	95.0	
								VTD59	0.028	0.009	43.5	95.0	
								VTD60	0.028	0.009	43.5	95.0	
Bus PP Chiller	0.400	98.760	-2.8	0.000	0.000	1.040	0.344	Bus PUTR 2	-1.040	-0.344	1609.3	95.0	
								VTD72	0.340	0.112	523.0	95.0	
								VTD73	0.340	0.112	523.0	95.0	
								VTD74	0.183	0.060	281.6	95.0	
								VTD75	0.183	0.060	281.6	95.0	
Bus PP CHWP	0.400	98.760	-2.8	0.000	0.000	0.447	0.274	Bus PUTR 2	-0.442	-0.274	759.9	85.0	
Bus PP VAC	0.400	98.760	-2.8	0.000	0.000	0.329	0.204	Bus PUTR 2	-0.329	-0.204	565.0	85.0	
Bus PUTR 1	0.400	98.804	-2.8	0.000	0.000	0.001	-1.001	Bus MDP EMG PARKIR	0.028	0.012	42.4	90.7	
								Bus MDP EMG	0.437	0.252	736.7	86.8	
								Bus 102	-1.987	-0.657	2901.4	100.0	
								Bus 179	1.363	0.486	2222.8	89.1	
								Bus sGP PARKIR	0.160	0.069	274.5	85.2	
Bus PUTR 2	0.400	98.760	-2.8	0.000	0.000	0.001	-0.769	Bus 97	-1.957	-0.118	2805.9	99.8	
								Bus PP Chiller	1.040	0.344	1609.3	95.0	
								Bus PP CHWP	0.442	0.274	759.9	85.0	

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### Lampiran 1.3 Lanjutan hasil simulasi load flow analysis

Project:		ETAP			Page:		11		
Location:		19.0.1C			Date:		23-08-2023		
Contract:					SN:				
Engineer:		Study Case: LF			Revision:		Base		
Filename:		skripsi.moy			Config.:		Normal		
Bus									
ID	kV	% Mag.	Ang.	MW	Mvar	MW	Mvar	ID	XFMR
Bus SDP Punggi PL	0.400	98.748	-2.8	0.000	0.000	0.026	0.012	Bus PUTRI.1	0.140
Bus PP-VAC	0.329	0.204	365.0	85.0					
Bus SDP EMO PARKIR	0.400	98.964	-2.8	0.000	0.000	0.160	0.099	Bus PUTRI.1	-0.160
VTD296								VTD297	0.007
VTD297								0.002	10.9
Bus SDP PARKIR	0.400	98.766	-2.8	0.000	0.000	0.140	0.087	Bus PUTRI.1	-0.140
Bus SDP Punggi PL	0.400	98.766	-2.8	0.000	0.000	0.140	0.087	Bus PUTRI.2	-0.140
* Bus17	20.000	100.000	0.0	1.974	0.218	9.000	0.000	Bus94	1.974
* Bus18	20.000	100.000	0.0	2.004	0.137	0.000	0.000	Bus103	2.004
Bus94	20.000	99.995	0.0	0.000	0.000	0.000	0.000	Bus17	-1.974
Bus95	20.000	99.995	0.0	0.000	0.000	0.000	0.000	Bus95	1.974
Bus95	20.000	99.995	0.0	0.000	0.000	0.000	0.000	Bus94	-1.974
Bus102	20.000	99.995	0.0	0.000	0.000	0.000	0.000	Bus103	2.004
Bus103	20.000	99.995	0.0	0.000	0.000	0.000	0.000	Bus102	-2.004
* VTD50	0.400	100.000	0.0	0.007	0.004	0.000	0.000	PP-LIETTEPL540-	0.007
* VFD57	0.400	100.000	0.0	0.007	0.004	0.000	0.000	PP-LIETTEPL555-	0.007
* VTD58	0.400	100.000	0.0	0.010	0.006	0.000	0.000	LIB(LS-05)-	0.010
* VTD59	0.400	100.000	0.0	0.027	0.017	0.000	0.000	LIB(LS-02)-	0.027
* VTD60	0.400	100.000	0.0	0.027	0.017	0.000	0.000	LIB(LS-01)-	0.027
* VTD61	0.400	100.000	0.0	0.007	0.014	0.000	0.000	LIB(P-01)-	0.007
* VTD62	0.400	100.000	0.0	0.007	0.004	0.000	0.000	LIB(P-02)-	0.007
* VTD63	0.400	100.000	0.0	0.015	0.009	0.000	0.000	LIB(LZ-03)-	0.015
* VTD64	0.400	100.000	0.0	0.015	0.009	0.000	0.000	LIB(LZ-05)-	0.015
* VTD65	0.400	100.000	0.0	0.015	0.009	0.000	0.000	LIB(LZ-03)-	0.015
* VTD66	0.400	100.000	0.0	0.015	0.009	0.000	0.000	LIB(LZ-04)-	0.015
* VTD67	0.400	100.000	0.0	0.074	0.048	0.000	0.000	LIB(LZ-04)-	0.074
* VTD68	0.400	100.000	0.0	0.074	0.035	0.000	0.000	LIB(LZ-07)-	0.056
* VTD69	0.400	100.000	0.0	0.066	0.000	0.000	0.000	LIB(HZ-07)-	0.066
* VTD70	0.400	100.000	0.0	0.056	0.035	0.000	0.000	LIB(HZ-02)-	0.056
* VTD71	0.400	100.000	0.0	0.056	0.035	0.000	0.000	LIB(HZ-01)-	0.056

**Lampiran 1.4 Lanjutan hasil simulasi *load flow analysis***

Project:	ETAP		Page:	12
Location:	19.0.1C		Date:	23-08-2023
Contract:			SN:	
Engineer:	Study Case: LF		Revision:	Bare
Filename:	skripsisey		Config.:	Normal

  

Bus	Voltage		Generation		Load		Load Flow				XFMR			
	ID	kV	% Mag.	Avg.	MW	Mvar	MW	Mvar	ID	MW	Mvar	Amp	%PF	%Sip
* VFD32		0.400	100.000	0.0	0.332	0.205	0.000	0.000	PC-CHILLER 1-	0.332	0.205	562.9	85.0	
* VFD33		0.400	100.000	0.0	0.332	0.205	0.000	0.000	PC-CHILLER 2-	0.332	0.205	562.9	85.0	
* VFD34		0.400	100.000	0.0	0.179	0.111	0.000	0.000	PC-CHILLER 3-	0.179	0.111	303.3	85.0	
* VFD35		0.400	100.000	0.0	0.179	0.111	-0.000	0.000	PP-CHILLER 4-	0.179	0.111	303.3	85.0	

\* Indicates a voltage regulated bus; voltage controlled or swing type machine connected to it.  
# Indicates a bus with a load mismatch of more than 0.1 MVA



# Skripsi Ahmad Aminul Kasbi

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3	<b>123dok.com</b> Internet Source		<b>1 %</b>
4	<b>Submitted to Universitas Nasional</b> Student Paper		<b>1 %</b>
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